

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A Newcard device operable to transfer power from a first subsystem to a second subsystem of a computer, the Newcard device comprising:
 - a first port electrically coupled to the first subsystem by a first connector, wherein the first connector includes at least one pin terminal to transfer the power and at least one pin terminal coupled to an SMbus of the computer;
 - a second port electrically coupled to the second subsystem by a second connector, wherein the second connector includes at least one pin terminal to transfer the power;
 - a transfer component electrically coupled to the first and second ports, wherein the transfer component is operable to transfer the power; and
 - a memory component electrically coupled to the SMbus, the memory component storing an identifier to uniquely identify the device, wherein an amount of the power received by the device is adjusted in response to the identifier.
2. (Original) The device of claim 1, wherein the first subsystem includes:
 - a detection component operable to detect the device; and
 - a control component operable to adjust the amount of the power transferred to the at least one pin terminal of the first connector in response to detection of the device.
3. (Original) The device of claim 2, wherein the detection component reads the identifier via the SMbus to detect the device.

4. (Original) The device of claim 2, wherein the control component adjusts the amount of power by adjusting a voltage of the at least one pin terminal of the first connector.
5. (Original) The device of claim 1, wherein the computer is partitioned into the first and second subsystems based on at least one predefined criteria.
6. (Original) The device of claim 1, wherein the second port includes at least one high speed serial communications bus, wherein the at least one high speed serial communications bus conforms to PCIE standard.
7. (Original) The device of claim 1, wherein the second port includes a second serial communications bus conforming to USB standard.
8. (Original) The device of claim 1, wherein the first connector includes 28 pins.
9. (Original) The device of claim 8, wherein a first set of at least one of the 28 pins includes the at least one pin terminal to transfer the power and a second set of at least one of the 28 pins includes the at least one pin terminal coupled to the SMBus.
10. (Original) The device of claim 1, wherein the first and second subsystems are coupled by two Newcard devices connected in parallel, wherein the two Newcard devices are substantially identical.
11. (Original) A method for transferring power from a first subsystem to a second subsystem of a computer via a Newcard device, the method comprising:

coupling the first subsystem to the device, wherein the coupling includes electrically connecting a first port of the device to the first subsystem by a first connector;

coupling the device to the second subsystem, wherein the coupling includes electrically connecting a second port of the device to the second subsystem by a second connector;

reading an identifier stored in the device to uniquely identify the device;
and

controlling the power transferred to the device in response to the identifier.

12. (Original) The method of claim 11, wherein the coupling of the first subsystem to the device further includes electrically connecting a separate power connector connected to a source of the power included in the first subsystem, wherein the separate power connector is coupled to an extended module member by a cable external to the device, wherein the extended module member is coupled to the second subsystem to transfer the power.
13. (Original) The method of claim 11, wherein the power transferred from the first subsystem to the second subsystem is independent of the first connector.
14. (Original) The method of claim 11, wherein the second connector includes at least one pin terminal to transfer the power.
15. (Original) The method of claim 11, wherein the first connector includes at least one pin terminal to transfer the power and at least one pin terminal coupled to an SMBus of the computer.

16. (Original) The method of claim 11, wherein the reading is performed by a detection component of the first subsystem and the controlling is performed by a control component of the first subsystem.
17. (Original) The method of claim 16, wherein the detection component reads the identifier via the SMBus.
18. (Original) The method of claim 16, wherein the control component controls the power transferred by adjusting a voltage of at least one pin terminal of the first connector.
19. (Original) The method of claim 11, wherein the computer is partitioned into the first and second subsystems based on at least one predefined criteria.
20. (Original) The method of claim 11, wherein the second port includes at least one high speed serial communications bus, wherein the at least one high speed serial communications bus conforms to PCIE standard.
21. (Original) The method of claim 11, wherein the second port includes a second serial communications bus conforming to USB standard.
22. (Original) The method of claim 11, wherein the first connector includes 28 pins.
23. (Original) The method of claim 22, wherein a first set of at least one of the 28 pins includes the at least one pin terminal to transfer the power and a second set of at least one of the 28 pins includes the at least one pin terminal coupled to the SMBus.

24. (Original) The method of claim 11, wherein the power is transferred from the first to the second subsystem by two Newcard devices connected in parallel, wherein the two devices are substantially identical.
25. (Original) An information handling system comprising:
- a first subsystem, wherein the first subsystem is defined to include:
 - a processor;
 - a system bus; and
 - a memory coupled to the processor through the system bus;
 - a second subsystem, wherein the second subsystem is defined to include at least one expansion card; and
 - a Newcard device operable to transfer power from the first subsystem to the second subsystem, wherein the device includes:
 - a first port electrically coupled to the first subsystem by a first connector, wherein the first connector includes at least one pin terminal to transfer the power and at least one pin terminal coupled to an SMBus of the system;
 - a second port electrically coupled to the second subsystem by a second connector, wherein the second connector includes at least one pin terminal to transfer the power;
 - a transfer component electrically coupled to the first and second ports, wherein the transfer component is operable to transfer the power; and
 - a memory component electrically coupled to the SMBus, the memory component storing an identifier to uniquely identify the device, wherein the first subsystem reads the identifier and adjusts an amount of the power transferred to the device in response to the identifier.

26. (Original) The system of claim 25, wherein the first subsystem adjusts the amount of power by adjusting a voltage of the at least one pin terminal of the first connector.
27. (Original) The system of claim 25, wherein the power is transferred from the first to the second subsystem by two Newcard devices connected in parallel, wherein the two devices are substantially identical.
28. (New) An information handling system comprising:
 a first subsystem, wherein the first subsystem is defined to include:
 a processor;
 a storage coupled to the processor; and
 a memory coupled to the processor;
 a second subsystem, wherein the second subsystem is defined to include at least one expansion card; and
 a Newcard device operable to transfer power from the first subsystem to the second subsystem, wherein the device includes:
 a first port electrically coupled to the first subsystem by a first connector, wherein the first connector includes at least one pin terminal to transfer the power and at least one pin terminal coupled to an SMBus of the system;
 a second port electrically coupled to the second subsystem by a second connector, wherein the second connector includes at least one pin terminal to transfer the power;
 a transfer component electrically coupled to the first and second ports, wherein the transfer component is operable to transfer the power; and
 a memory component electrically coupled to the SMBus, the memory component storing an identifier to uniquely identify the device,

wherein the first subsystem reads the identifier and adjusts an amount of the power transferred to the device in response to the identifier.